



Encore's G.168

Processor

TMS320C55x Series.

Technology

Encore's G.168 is an ITU-T G.168 compliant line echo canceller. The Echo Canceller cancels electrical echo arising from 2/4 wire hybrid interface impedance mismatches.

Features

- Maximum echo tail can be configured at the time of echo canceller channel initialization. Possible echo tails are 16 msec, 32 msec or 64 msec.
- Texas Instruments eXpressDSP™ compatible software architecture.
- Double talk detection to avoid divergence in case of double talk.
- Non-linear processing (NLP), which can be enabled or disabled during channel initialization.
- Automatic disabling of adaptation for Fax answer tone (2100 Hz with periodic phase reversal).
- Flexible interface with 'C' callability, with a single archive file for all functions.
- Built-in scratch memory management to avoid run-time overloading of system stack memory.
- Relocatable program and data spaces. Static (state) and scratch data spaces are dynamically relocatable. Program and table data spaces can be fragmented.
- The code is interruptible and re-entrant. It can be used in systems with multi threaded software architecture.

Performance

Resource Requirements

Memory (KBytes)

Program Memory	Data Memory	
	Static/Channel	Scratch
2.384	1.090	1.156

MIPS

MIPS measurements are taken on **TMX320VC5510** based target platform with following memory configurations, for 32 msec echo tail.

Program Memory		SARAM
Data Memory	Static	DARAM
	Scratch	DARAM

	Maximum
NLP Disabled	5.10
NLP Enabled	5.12

Availability

Now.

For further information please visit our web site, <http://www.ncoretech.com> or email to: ip@ncoretech.com or contact:

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