



Encore's V.32/V.32bis Modem

Technology

This implementation is based on the V.32bis, V.22bis, V.21 and V.23 Recommendations. All the data rates ranging from 14.4 Kbps down to 300 bps are supported. The implementation handles full-duplex data transmission.

Features

- Adheres to all the mandatory features of ITU-T V.32/V.32bis Standard.
- Supports all other V- Series modulation modes including V.22bis, V.21, V.23
- Channel separation by echo cancellation techniques.
- Employs QAM and both TCM and Non-TCM coding
- Automodems to V-Series modems supported by V.32bis automode procedures.
- Support for both V.8 and Non-V.8 procedures during mode startup procedure for modulation modem selection
- HDLC Framing Support.
- Multi Channel Capability
- Highly optimized implementation
- Relocatable Code & Data Modules.
- Flexible Programming Interface ('C' Callable).
- Tested against various impairment combinations using Standard Test Equipment (TAS).
- Provided in the form of Hardware Independent Library for easy pointing to the target platform.
- ITU-T V.42 – Error control protocol and V.42bis data compression modules can either be available as part of this package or additional modules.

Platforms

- TMS320C64x
- TMS320C62X
- TMS320C54x
- Microchip's dsPIC
- ARM-9

Performance Numbers

Platform	Program Memory (KBytes)	State Memory (KBytes)	Scratch (KBytes)	Tables (KBytes)	MIPS/MCPS
TMS320C64x	81	10*	0	4.7	9.5
TMS320C62x	91	10*	0	4.7	9
TMS320C54x	20	7*	0	5.8	16
dsPIC	32	3.1*	0	5	15
ARM-9	36	7.4*	0	5	50**

* Specifications are subject to change

- Numbers does not include V.42/V.42bis modules
- * In addition to this, 960 bytes are required for every 100ms bulk delay.
- ** Measured with 16 KBytes I Cache and 16 KBytes D Cache

Availability

Now

For further information please visit our web site, <http://www.ncoretech.com> or email to: ip@ncoretech.com