



Encore's V.42/V.42bis Protocol

Technology

This implementation is based on the ITU-T V.42/V.42bis error control and data compression protocols.

Features

- Adheres to all the mandatory features of ITU-T V.42/V42bis specifications.
- V.42 implements an HDLC based protocol referred to as the Link Access Procedure for Modems (LAPM).
- Error detection through the use of a cyclic redundancy check.
- Error correction through the use of automatic retransmission of data.
- synchronous transmission through the conversion of start-stop data to HDLC frames
- An initial handshake in start-stop format which minimizes disruption to the DTEs
- V.42bis provides improved throughput through the use of data compression procedures.
- Compression procedure based on an algorithm which encodes strings of characters
- Decoding procedure which recovers the strings of characters from received codewords
- An automatic transparent mode of operation when uncompressible data is detected.
- Multi Channel Capability
- Highly optimized implementation
- Relocatable Code & Data Modules.
- Flexible Programming Interface ('C' Callable).
- Integrated with Encore's data pump software and tested.
- Provided as an independent library and hence can be integrated with any data pump software.
- Available as C code and hence can be easily ported to any platform.
- Available as V.42 alone or combined V.42/V.42bis modules.

Platforms

- TMS320C64x
- TMS320C62X
- TMS320C54x
- Microchip's dsPIC
- ARM-9

Performance Numbers

Platform	Program Memory (KBytes)	State Memory (KBytes)	Scratch (KBytes)	Tables (KBytes)	MIPS/MCPS
TMS320C64x	36	21*	0	0	7
TMS320C62x	40	21*	0	0	7
TMS320C54x	22	21*	0	0	8
dsPIC**	14	2	0	0	1.5
ARM-9	21	21*	0	0	6***

* Specifications are subject to change

- * Window size of 15 and dictionary size of 1024.
- ** Window size of 5 and No V.42bis support
- *** Measured with 16 KBytes I Cache and 16 KBytes D Cache

Availability

Now

For further information please visit our web site, <http://www.ncoretech.com> or email to: jp@ncoretech.com