



# Encore's T.38 Fax Relay

## Technology

The implementation is a real time fax relay; supports ITU-T T.38 along with fax data pump ITU-T V.17, V.29, V.27ter and V.21 Protocols.

## Features

- Adheres to all the mandatory features of ITU-T T.38.
- Support up to and including 14,400 bps fax (ITU-TV.17, V.29 and V.27ter).
- Interoperability tested against T.38 gateways and routers and T.38 enabled ATAs.
- Supports redundancy for error protection in UDP mode of packet transfer.
- Spoofing technique implemented so as to handle fixed latency and jitter in receiving IFP packets.
- Multi Channel Capability.
- Highly optimized implementation.
- Relocatable Code & Data Modules.
- Flexible Programming Interface ('C' Callable).
- Inter operability tested using commercial fax machines and the Genoa Technology's FAXLAB™ T.30 test suite to ensure seamless T.30 relay.
- Tested for various IP network impairments such as latency, jitter, packet losses, out of order packet and duplicate packets.
- Provided in the form of Hardware Independent Library for easy pointing to the target platform.

## Platforms

- TMS320C55x
- BlackFin (ADSP BF53x Series)
- ZSP400
- ColdFire (MCF52xx, 53xx, 522xx)
- PowerPC
- ARM9E

## Performance Numbers<sup>1</sup> (T.38+DP)

| Platform             | Program Memory (K Bytes) | State Memory (K Bytes) | Scratch (K Bytes) | Tables (K Bytes) | MIPS/MCPS |
|----------------------|--------------------------|------------------------|-------------------|------------------|-----------|
| TMS320C55x           | 44                       | 23                     | 0                 | 4.8              | 13.9      |
| BF53x <sup>2</sup>   | 50                       | 18                     | 0                 | 4.8              | 24        |
| ZSP400               | 36                       | 18                     | 0                 | 4.8              | 14.1      |
| MCF52xx, 53xx, 522xx | 54                       | 16                     | 0                 | 4.65             | 34        |
| PowerPC              | 120                      | 16                     | 0                 | 4.65             | 26        |
| ARM9E <sup>3</sup>   | 53                       | 14                     | 2                 | 4                | 19        |

<sup>1</sup> Specifications are subject to change

<sup>2</sup> MIPS indicated for BlackFin platform are for program and data residing in external memory and being cached.

<sup>3</sup> For ARM9E, all the data memory blocks need 32-bit alignment. MCPS numbers are assuming flat memory access.

## Availability

Now

For further information please visit our web site, <http://www.ncoretech.com> or email to: [ip@ncoretech.com](mailto:ip@ncoretech.com)