



Encore's EVRC

Technology

The Enhanced Variable Rate Coder (EVRC) for CDMA is based on 3 codecs: full rate at 8.5 kbps, half rate at 4 kbps, and eighth rate at 800 bps.

The coder works on a frame of 160 speech samples (20 msec) and requires a look ahead of 80 samples (10 msec) for noise-suppression. So the total algorithmic delay for the coder is 30 msec.

Features

- Fully compatible with the following TIA/EIA standard
IS-127 Enhanced Variable Rate Codec, Speech Service Option 3 for Wideband Spread Spectrum Digital Systems (1997)
- Verification of bit-exactness as per the following TIA/EIA standard
IS-718 Minimum Performance Standard for the Enhanced Variable Rate Codec, Speech Service Option 3 for Spread Spectrum Digital Systems (1998)
- Supports Noise Suppression.
- Full duplex multi-channel capability.
- Flexible interface with 'C' callability, with a single archive file for all functions.
- Built-in scratch memory management to avoid run-time overloading of system stack memory.
- Relocatable program and data spaces. Static (state), Scratch data are dynamically relocatable. Program space can be fragmented.
- The code is interruptible and frame re-entrant. It can be used in systems with multi threaded software architecture.
- Availability in little endian format.

Platforms

- TMS320C64X
- TMS320C62X

Performance Numbers

Platform	Program Memory (KBytes)	Data Memory (KBytes)			MIPS
		Static/Channel	Scratch	Tables	
TMS320C64X	146.2	4.34	5	14.5	13.5
TMS320C62X	146.2	4.34	5	14.5	14.7

Availability

Now

For further information please visit our web site, <http://www.ncoretech.com> or email to: ip@ncoretech.com