



# Encore's G.168

## Technology

Encore's G.168 is an ITU-T G.168 compliant line echo canceller. The Echo Canceller cancels electrical echo arising from 2/4 wire hybrid interface impedance mismatches.

## Features

- Maximum echo tail can be configured at the time of echo canceller channel initialization. Possible echo tails are 16 msec, 32 msec, 64 msec or 128 msec.
- Double talk detection to avoid divergence in case of double talk.
- Non-linear processing (NLP), which can be enabled or disabled during channel initialization.
- Automatic disabling of adaptation for Fax answer tone (2100 Hz with periodic phase reversal).
- Flexible interface with 'C' callability, with a single archive file for all functions.
- Built-in scratch memory management to avoid run-time overloading of system stack memory.
- Relocatable program and data spaces. Static (state) and scratch data spaces are dynamically relocatable. Program and table data spaces can be fragmented.
- The code is interruptible and frame re-entrant. It can be used in systems with multi threaded software architecture.

## Platforms

- TMS320C64X
- TMS320C62X
- TMS320C54X

## Performance Numbers (for 64 msec tail length)

Platform	Program Memory (KBytes)	Data Memory (KBytes)			MIPS
		Static/Channel	Scratch	Tables	
TMS320C64X	16.63	3.03	2.5	0.17	5.78
TMS320C62X	16.63	3.03	2.5	0.17	7.0
TMS320C54X	3.0	3.03	2.5	0.17	**5.5
Motorola PowerPC 8248	11.63	3.03	2.5	0.17	**24

\*\* measured for 32 msec tail length.

## Availability

Now

For further information please visit our web site, <http://www.ncoretech.com> or email to: [jp@ncoretech.com](mailto:jp@ncoretech.com)